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Substitute for Tour 1449/PTC

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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Co	mplete if Known	1
Application Number	10/763,523	
Filing Date	January 23, 2004	
First Named Inventor	Kishio Yokouchi	_
Art Unit	1756	
Examiner Name	Unknown	
Attorney Docket Number	02EK-105599	_

			U. S. PATENT	DOCUMENTS	
Examiner Initials*	Cite No.'	Document Number  Number-Kind Code <sup>2 (J Amount)</sup>	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
JM	A1	<sup>US-</sup> 6343171	01-29-2002	Yoshimura, et al.	
JM	A2	<sup>US-</sup> 6400855	06-04-2002	Li, et al.	
JM	A3	<sup>US-</sup> 6504966	01-07-2003	Kato, et al.	
JM	A4	<sup>US-</sup> 6603915	08-05-2003	Glebov, et al.	
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		US-			

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Examiner Initials*	Cite No.1	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages	
		Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>3</sup> (if known)	MM-DD-YYYY		Or Relevant Figures Appear	₽
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Signature	/John McPherson/	Considered	01/02/2007

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STATEMENT BY APPLICANT			PPLICANT	First Named Inventor	Kishio Yokouchi
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Sheet	2	of	2	Attorney Docket Number	02EK-105599

		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
JM	C1	L. Dalton, et al., "Polymeric Electro-optic Modulators: From Chromophore Design to Integration with Semiconductor Very Large Scale Integration	
		Electronics and Silica Fiber Optics," Ind. Eng. Chem. Res., January 1999, pp. 8-33, Vol. 38, No. 1.	
JM	C2	S. Garner, et al., "Three-Dimensional Integrated Optics Using Polymers IEEE JOURNAL OF QUANTUM ELECTRONICS," August 1999,	
		pp.1146 -1155, Vol. 35, No. 8.	
JM	СЗ	Glebov, et al., "Two-dimensional microlens arrays in silica-on-silicon planar lightwave circuit technology," J. Microlith., Microfab., Microsyst.,	
		October 2003, pp. 309-318, Vol. 2 No. 4.	

Examiner	/ Tahn Wallhaman /	Date	01/02/2007	
Signature	/John McPherson/	Considered	01/02/2007	

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